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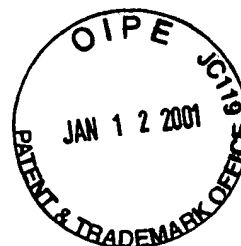
Assistant Commissioner for Patents
Washington, D.C. 20231

#4a
PATENT
Attorney Docket No.: 0939A-036911US
Client Reference No.: bkClientRef

On _____

TOWNSEND and TOWNSEND and CREW LLP

By: _____



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Talbot et al.

Application No.: 09/662,068

Filed: September 14, 2000

For: MEMORY REQUEST
REORDERING IN A DATA
PROCESSING SYSTEM

Examiner: Unassigned

Art Unit: 2185

PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above-referenced application, please enter the following amendments and remarks.

IN THE CLAIMS:

Please delete claims 1 - 52 without prejudice. Please add new claims 53 - 67

53. (NEW) In a data processing system, a method for ordering a plurality of memory access requests, the method comprising:
accepting the plurality of memory access requests;

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